

# INFORMATION DISCLOSURE CITATION IN AN APPLICATION

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ATTY. DOCKET NO.  
043876-0145

SERIAL NO.  
10/646,787

APPLICANT  
HANSEN, C., et al.

FILING DATE  
August 25, 2003

GROUP  
2183

## U.S. PATENT DOCUMENTS

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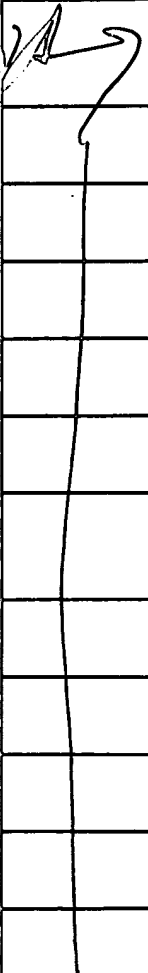
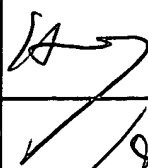
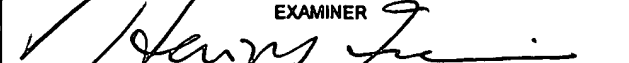
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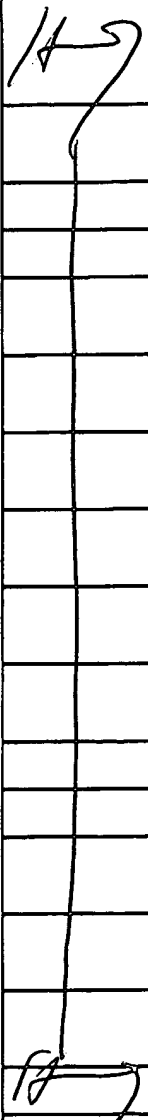
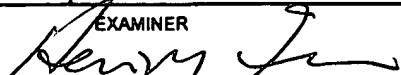
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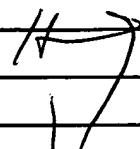

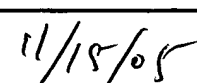
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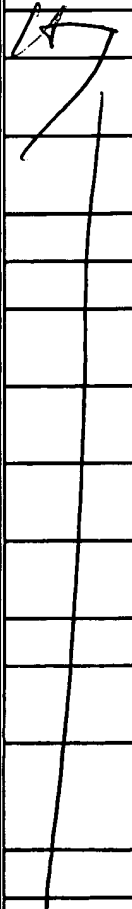
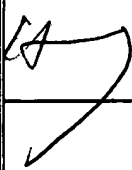
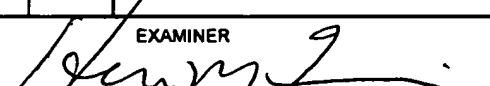
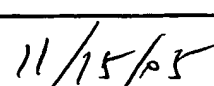
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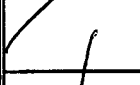
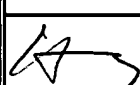
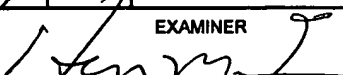
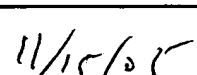
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		<b>APPLICANT</b> <b>HANSEN, C., et al.</b>	
		<b>FILING DATE</b> <b>August 25, 2003</b>	<b>GROUP</b> <b>2183</b>
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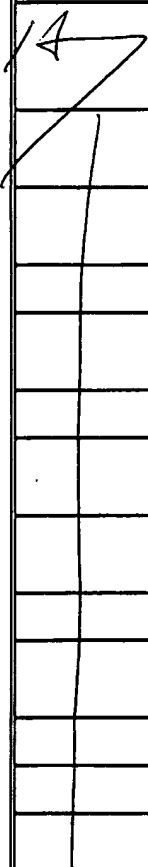
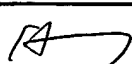
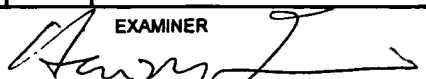
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